

FIG. 1

RELATED ART

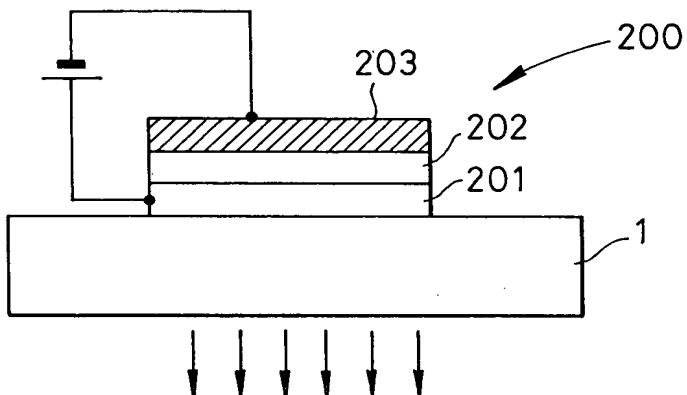


FIG. 2

RELATED ART

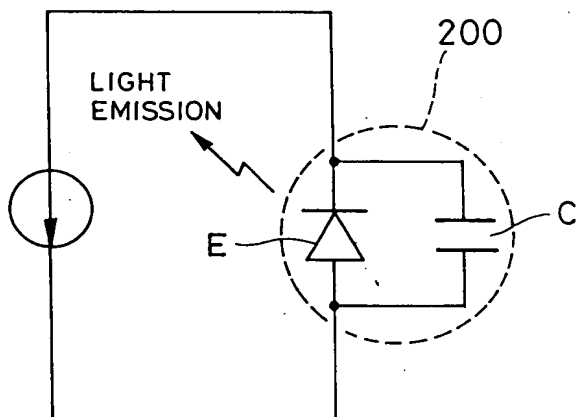


FIG. 3

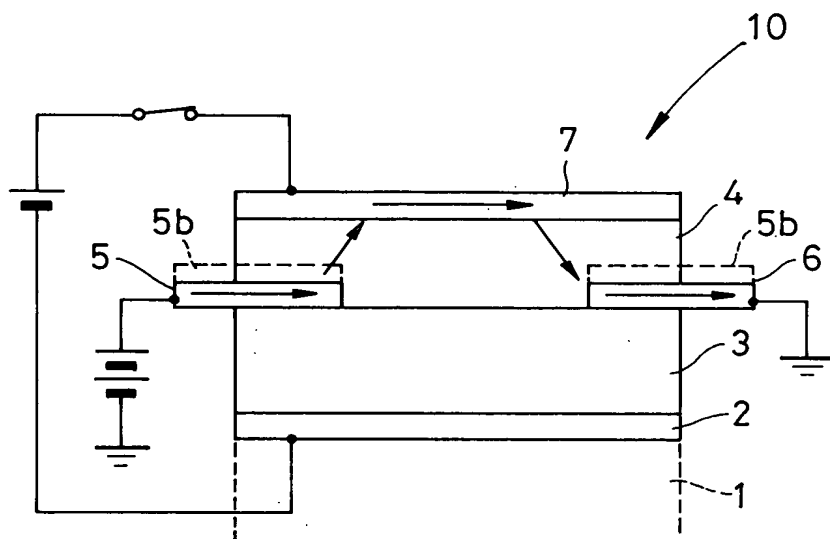


FIG. 4

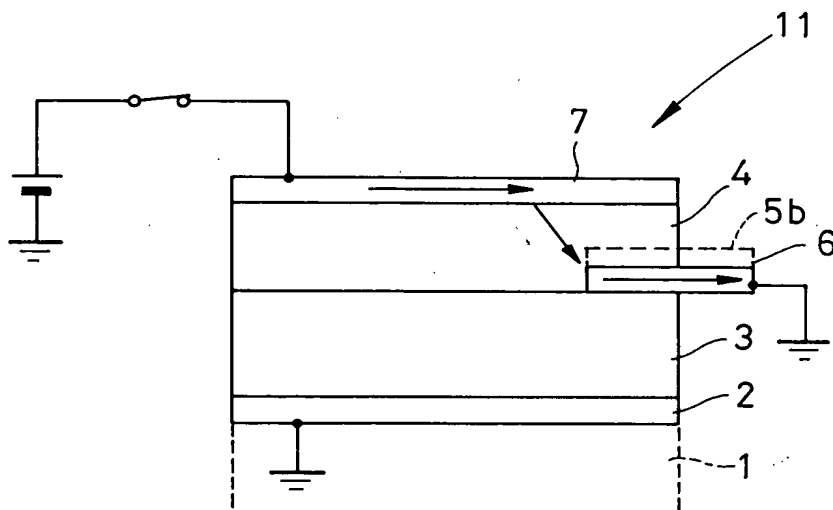


FIG. 5

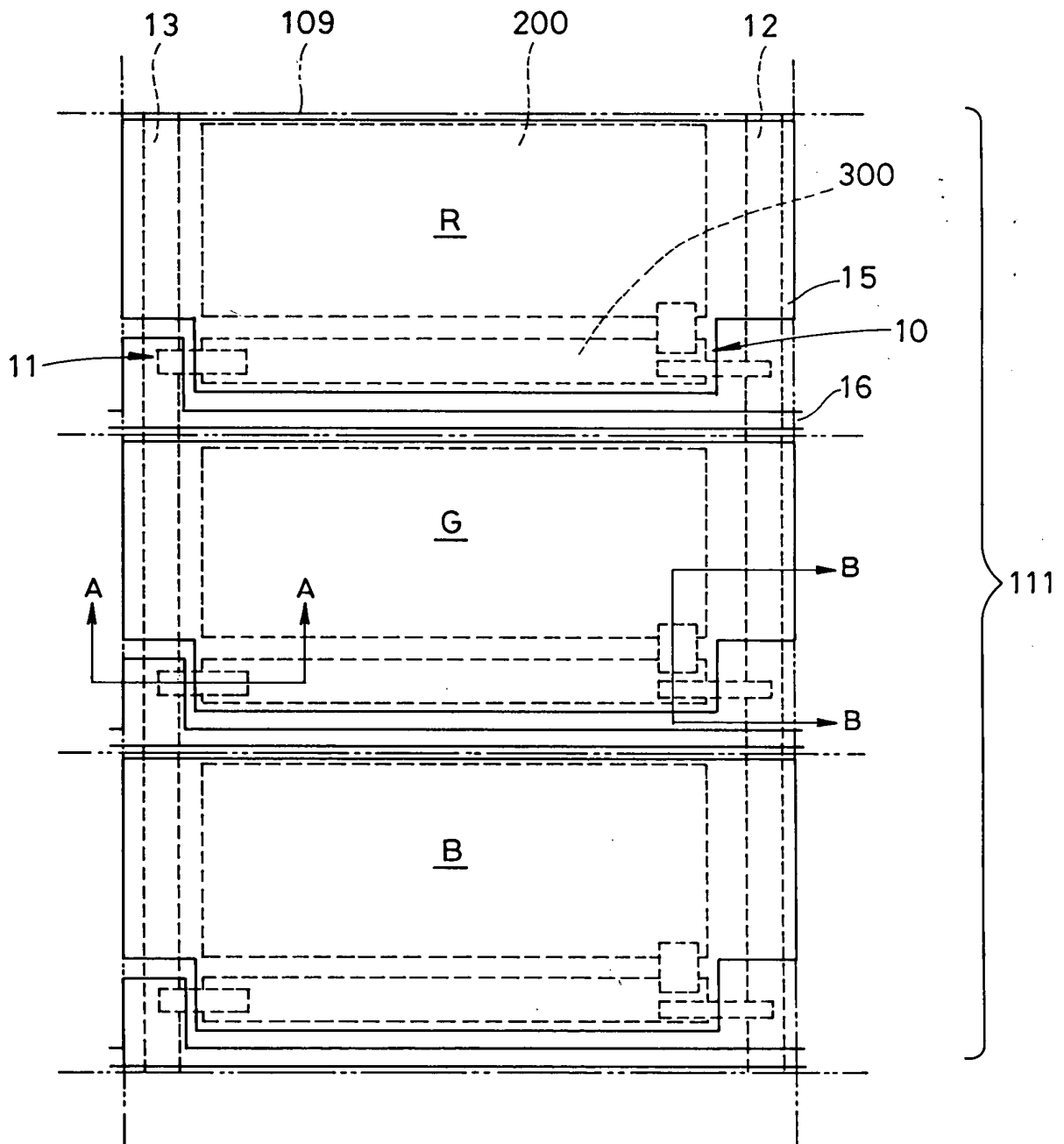


FIG. 6

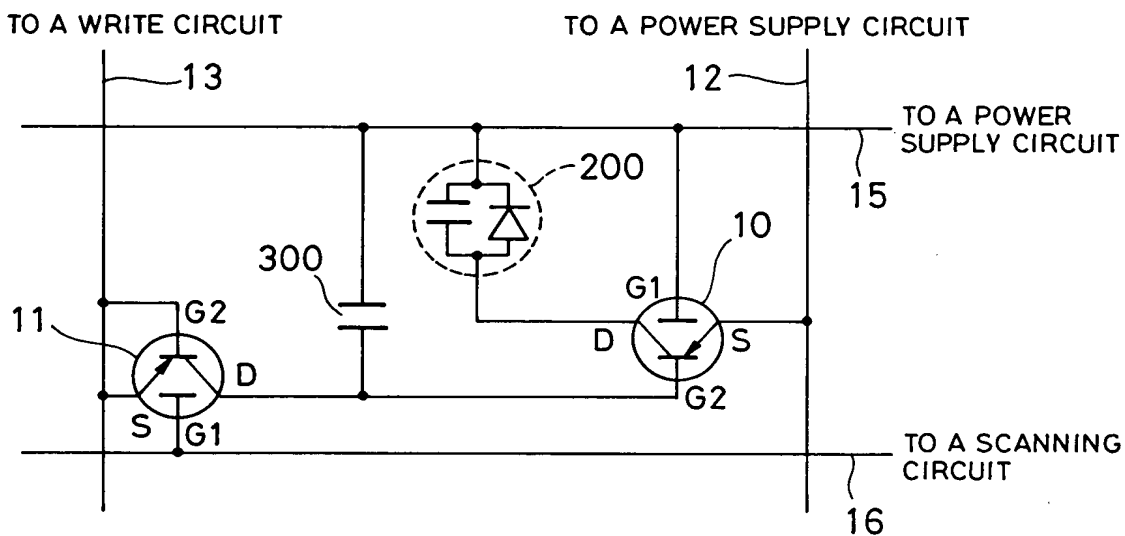


FIG. 7

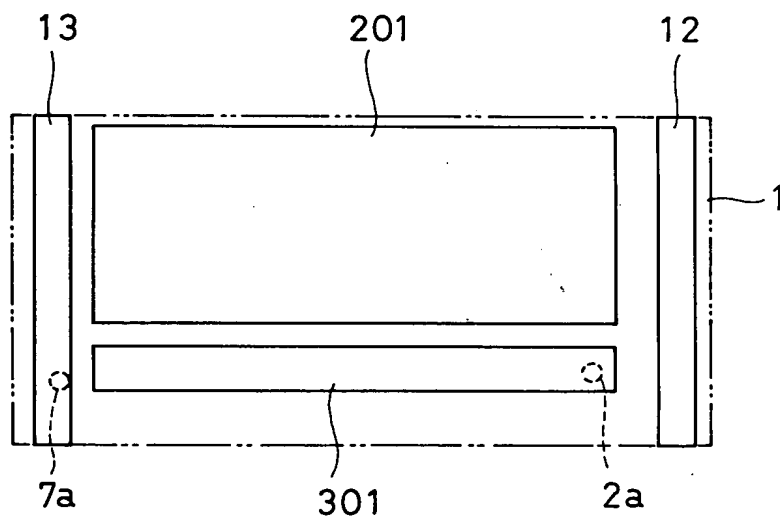


FIG. 8

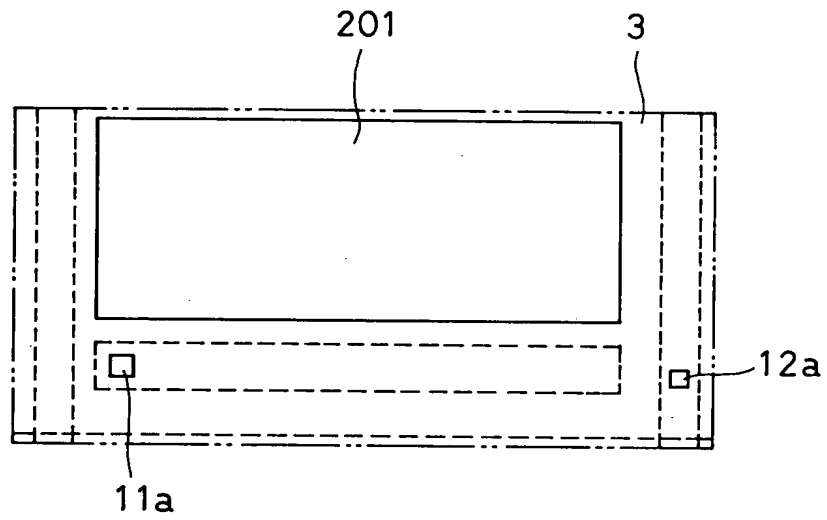
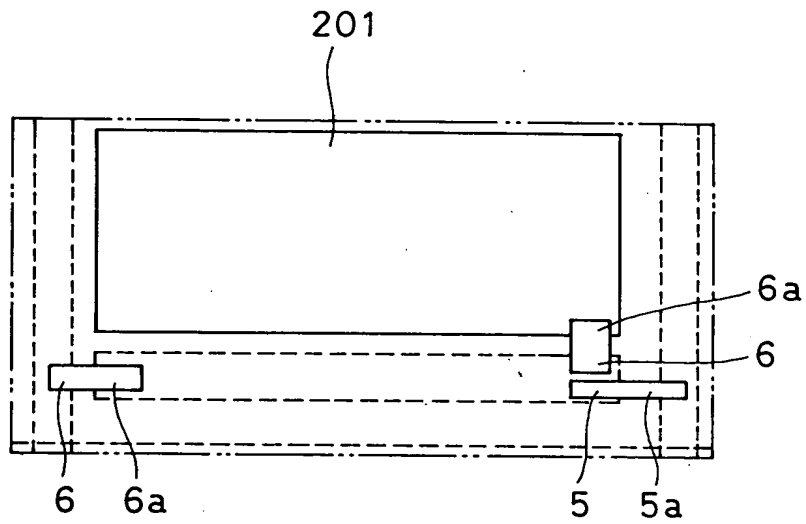


FIG. 9



6/12

FIG.10

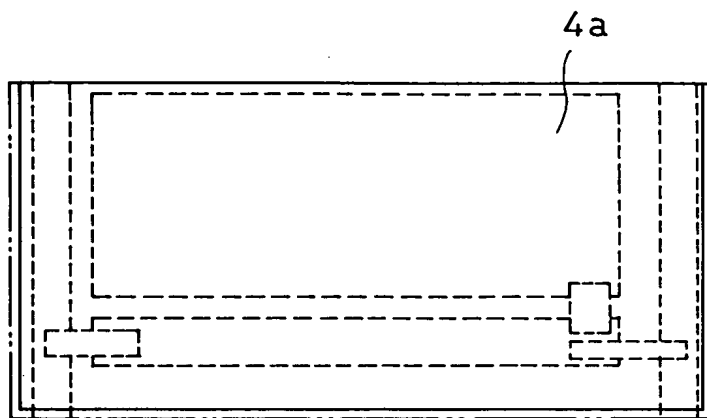


FIG.11

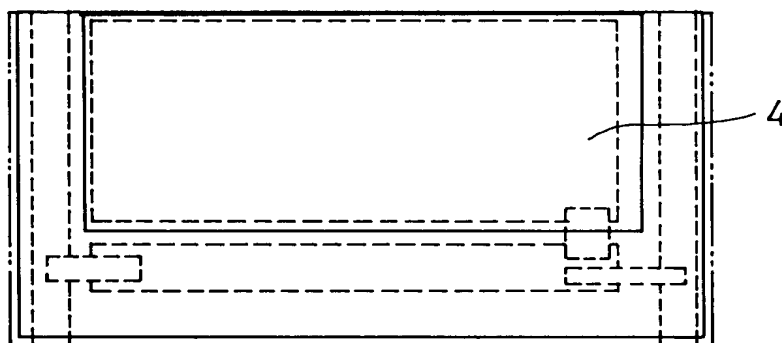


FIG.12

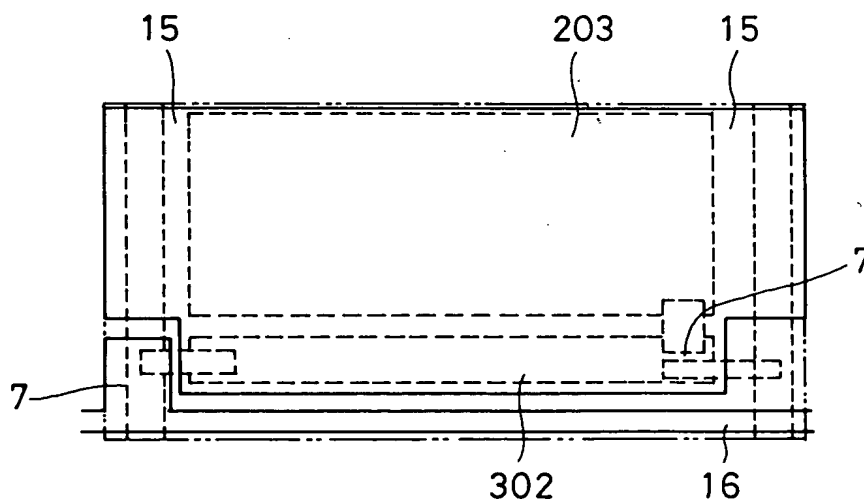


FIG. 1 is a block diagram of a video processing system. The system includes an input terminal connected to block 101. Block 101 is connected to block 102, which is connected to block 103. Block 103 is connected to block 106. Block 106 is connected to block 109 via a vertical bus. Block 109 is connected to block 107. Block 107 is connected to block 108. Block 108 is connected to block 104. Block 104 is connected to block 103. Block 104 is also connected to block 105. Block 105 is connected to block 106. Block 105 is also connected to block 109.

FIG.17

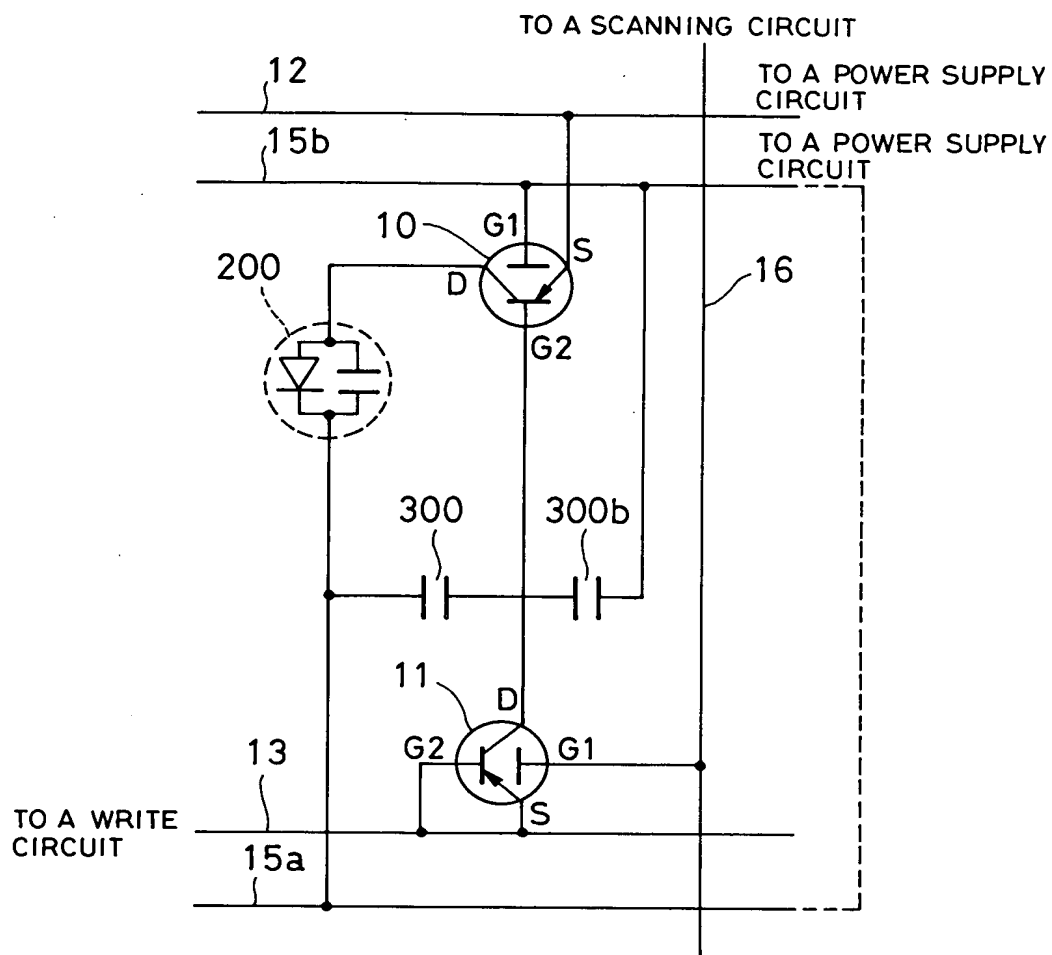
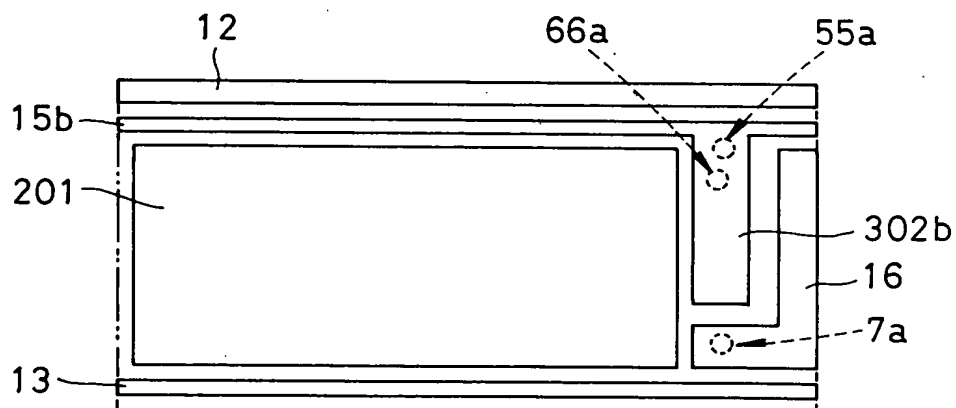


FIG. 18



10/12

FIG.19

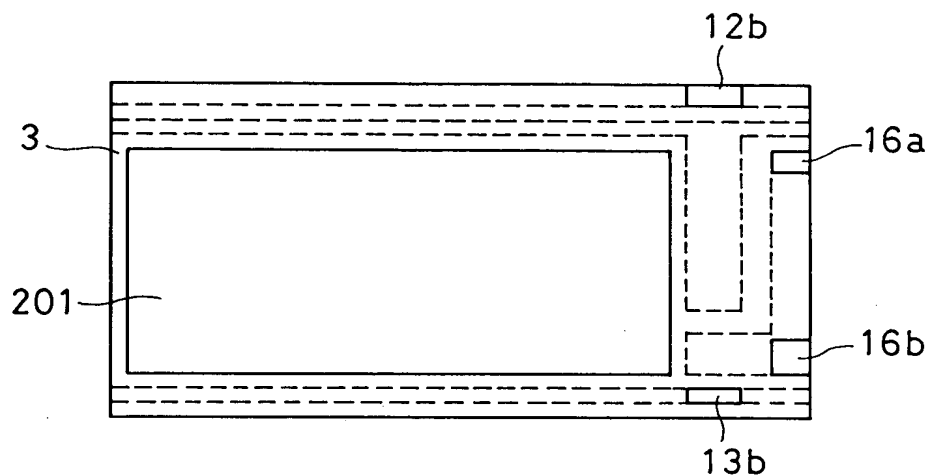


FIG.20

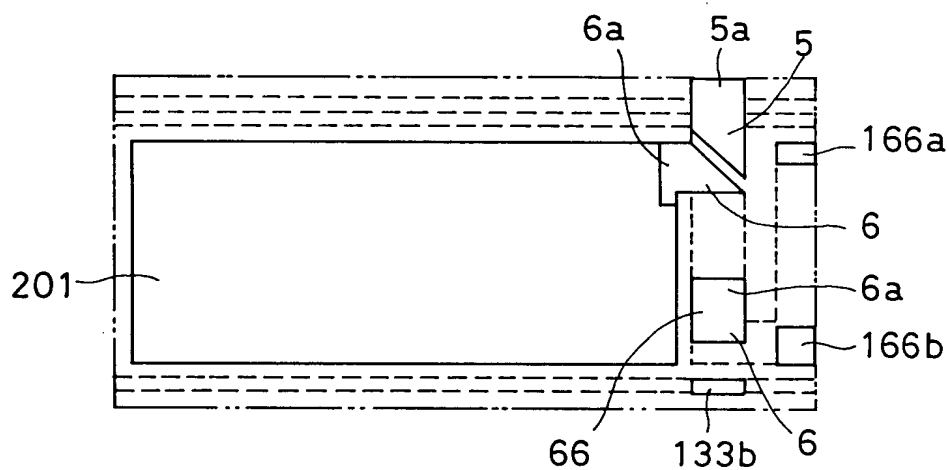


FIG.21

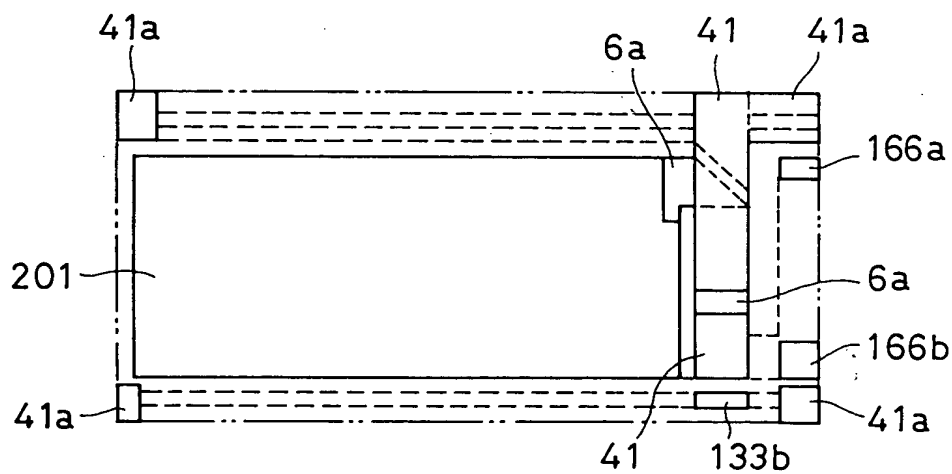


FIG. 22

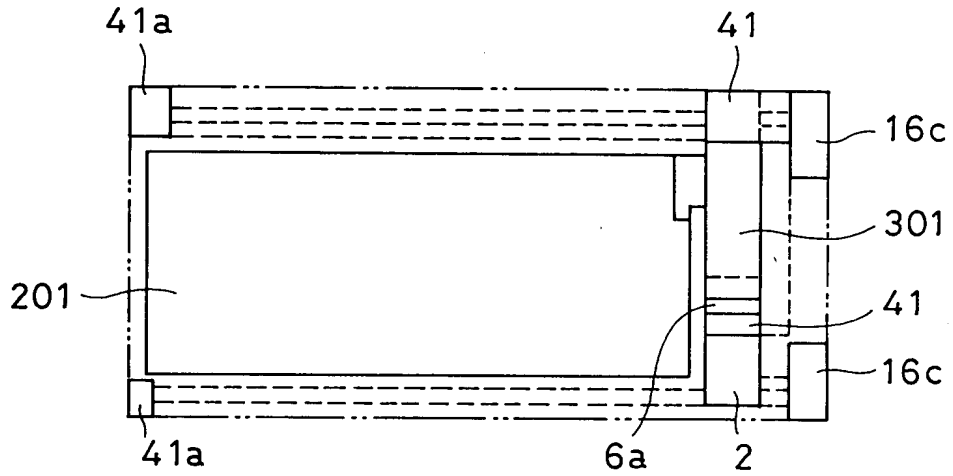


FIG. 23

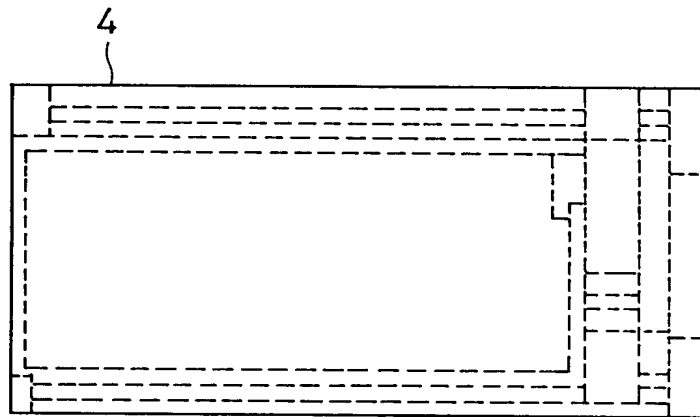


FIG. 24

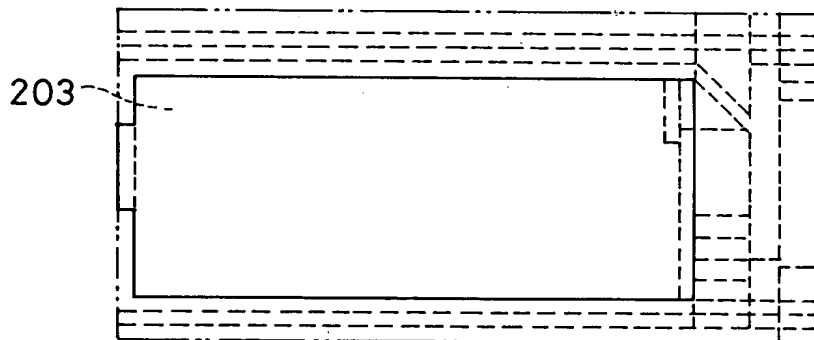


FIG. 25

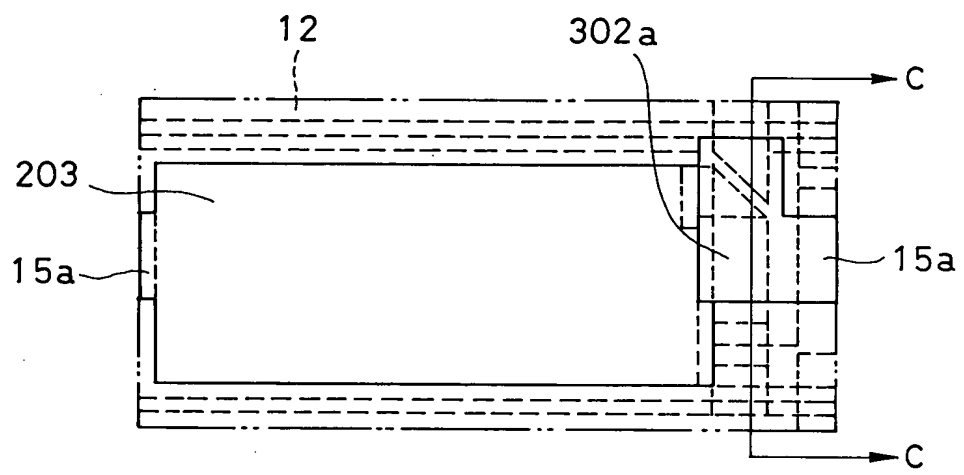


FIG. 26

